



# GEN5 PCIe Card and Drive Breaker Modules

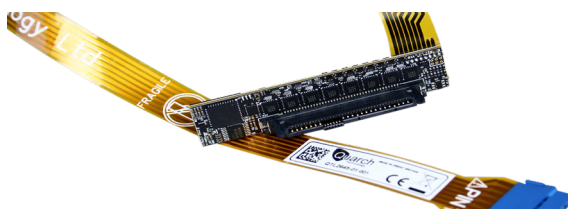
Automate hot-plug, dual redundancy and fault injection testing for GEN5 PCIe card devices

Quarch  
Data Sheet



# GEN5 PCIe Card and Drive Breaker Modules

Automate hot-plug, dual redundancy and fault injection testing for GEN5 PCIe devices



## Highlights

- Supports the full range of PCIe devices
- Removes manual intervention, for fully automated testing
- Precise and consistent timing control over hot-swap scenarios
- Completely transparent at the protocol layer
- Create and test many different fault conditions
- Simple to control with your existing test automation system

## Use Cases

<b>System Qualification</b>	Run repeated test cycles with bounds testing of all possible hot-swap and lane width scenarios
<b>Regression Testing</b>	Automated regression tests spot issues earlier during development
<b>RAID Testing</b>	Force drive rebuilds, single/double RAID faults
<b>Failover Testing</b>	Test dual redundancy, fault monitoring and performance during a failure
<b>Fault Injection</b>	Simulate a large number of fault scenarios





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## Hot Swap

PCIe data is switched with advanced high speed RF switches, ensuring that our modules are almost totally transparent to the storage system. Host/Device connections will appear as if they are directly attached.

Individual control over each pin allows us to create almost any possible hot-swap or fault scenario. Precise timing ensures that every test can be exactly re-created. Versions are available with inrush current limits, to help high power devices hot-plug on hosts with limited power supply capacity.

The modules can be manually controlled for bench testing, or easily integrated into your existing test automation system as part of a fully automated test solution.

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## Module Range

The Gen5 range is expanding as the interface gains traction. If you do not see the module you require, please let us know and we can get a time scale for you.

**NOTE:** Due to the signal intergity issues around early Gen5 devices, we request you evaluate a module in your test system before purchase.

The modules also switch the PCIe lanes and have an additional injection port to allow power margining and measurement from our Programmable Power Module.

All modules support data rates up to 32GT/s.

Active signal driving is support for signals such as PERST, CLKREQ and WAKE. The exact signals driven varies from module to module

With the '+Triggering' option, sideband monitoring allows you to query the

state of a sideband, or even divery the state out of the triggering port, for easy connection to a scope or analyzer

Interface options depend on the controller you chose, but include simple Serial, USB and LAN options. These can be accessed from almost any scripting language. You will need to purchase a separate controller to use this module.

Drive modules can be combined with other Torridon modules as part of a full test-automation system.

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## Supplied Parts

Each module comes with a 40cm interface cable, for connection to a controller.

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## Also Required

**Controller** - You will require one slot on a Torridon Controller for each Cable Module

**Downloads** - Our website contains many useful downloads to help you get started: [www.quarch.com](http://www.quarch.com)

USB Drivers

Technical Manuals

Quick Start Guides

Example Scripts

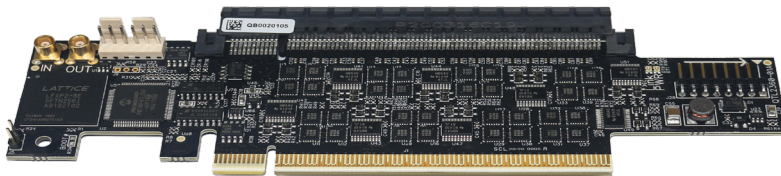
TestMonkey GUI



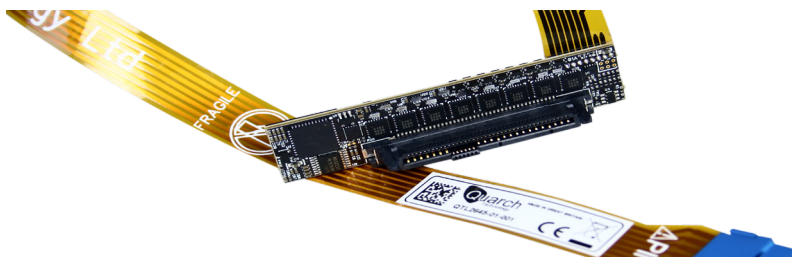


## Products Versions

Product Code	Product Options
<b>QTLXXXX</b>	Product code, made up from options below
<b>QTL2357</b>	Gen5 PCIe x16 Breaker Module
<b>QTL2358</b>	Gen5 PCIe x16 Breaker Module + Triggering
<b>QTL2396</b>	Gen5 PCIe x16 Breaker Module + Inrush Limit
<b>QTL2798</b>	Gen5 PCIe x16 Breaker Module + Triggering + Inrush Limit
<b>QTL2652</b>	Gen5 PCIe x16 Lite Breaker Module
<b>QTL2658</b>	Gen5 PCIe x16 Lite Breaker Module + Inrush Limit
<b>QTL2645</b>	Gen5 PCIe U.2 Breaker Module
<b>QTL2651</b>	Gen5 PCIe U.2 Breaker Module + Triggering
<b>QTL2662</b>	Gen5 PCIe U.3 Breaker Module
<b>QTL2661</b>	Gen5 PCIe U.3 Breaker Module + Triggering
<b>QTL2757</b>	Gen5 SFF Lite Breaker
<b>QTL2686</b>	Gen5 EDSFF E3 x4 Breaker Module
<b>QTL2692</b>	Gen5 EDSFF E3 x4 Breaker Module + Triggering



x16 Card Module



U.2 Drive Module



EDSFF E3 Drive Module



## Required Controllers - One port on a controller is required for each module

Product Code	Description	
<b>QTL1260</b>	<b>Torridon Interface Kit</b> Simple USB and Serial control options for bench testing	
<b>QTL1461</b>	<b>4 Port Torrison Controller</b> Control up to 4 modules via Serial/LAN/USB connection	
<b>QTL1079</b>	<b>28 Port Torrison Controller</b> Control up to 28 modules via Serial, LAN or USB connection	

## Accessories

Product Code	Description	
<b>QTL999</b>	<b>HD Programmable Power Module</b> Power margining any uA range power measurement, ideal for PCIe devices	
<b>QTL1558</b>	<b>40cm Torrison Double Ended Interface Cable (Female to Female)</b> Replacement cable for Card Modules, connects Module to Controller	
<b>QTL1870</b>	<b>100cm Torrison Double Ended Interface Cable (Female to Female)</b> Replacement cable for Card Modules, connects Module to Controller	
<b>QTL1381</b>	<b>100cm Torrison Extension Cable (Male to Female)</b> Extends an existing Double Ended Torrison cable or fixed Drive Module Cable	



### Technical Information

Connections	QTL2357	QTL2358	QTL2396	QTL2798	QTL2652	QTL2658
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<b>Host Side Connector</b>	PCIe x16					
<b>Device Side Connector</b>	PCIe x16					
<b>Max Speed</b>	32GT/s					
<b>Protocols</b>	PCIe					
<b>Signals Switched</b>	All <sup>1</sup>			Non Data <sup>2</sup>		

Connections...	QTL2645	QTL2651	QTL2757	QTL2661	QTL2662	QTL2686	QTL2692
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<b>Host Side Connector</b>	SFF-8639				EDSFF x4		
<b>Device Side Connector</b>	SFF-8639				EDSFF x8		
<b>Max Speed</b>	32GT/s						
<b>Protocols</b>	PCIe		PCIe, SAS			PCIe	
<b>Signals Switched</b>	All U.2 <sup>1</sup>		Non Data <sup>2</sup>	All U.3 <sup>1</sup>	All U.3 <sup>1</sup>	All <sup>1</sup>	

<sup>1</sup>All power, high speed data, mated and sideband pins are individually switched. GND pins are directly routed through the module.  
<sup>2</sup>High speed data is directly routed. Sidebands and power is switched (some are switches as a group)

Control	QTL2357	QTL2358	QTL2396	QTL2798	QTL2652	QTL2658
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<b>Power Supply</b>	Via Torridon Controller					
<b>Control Ports</b>	Torridon Connector					
<b>Triggering</b>	X	SMA	X	SMA	X	X
<b>Power Injection Port</b>	√	√	√	√	X	X

Control...	QTL2645	QTL2651	QTL2757	QTL2661	QTL2662	QTL2686	QTL2692
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<b>Power Supply</b>	Via Torridon Controller						
<b>Control Ports</b>	Torridon Connector						
<b>Triggering</b>	X	SMA	X	SMA	X	X	SMA
<b>Power Injection Port</b>	X	X	X	X	X	X	X





Dimensions	QTL2357	QTL2358	QTL2396	QTL2798	QTL2652	QTL2658
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Offsets Drive By	46.75mm			42.38mm		
Length/Width	167.67mm			167.65mm		
Height	-					
Compatible Devices	x1 - x16 PCIe Cards					

Dimensions...	QTL2645	QTL2651	QTL2757	QTL2661	QTL2662	QTL2686	QTL2692
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Offsets Drive By	11.86mm			35mm			
Length/Width	69.05mm			76mm			
Height	15.9mm			7.5mm			
Compatible Devices	U.2	SFF	U.3	U.3	x4 EDSFF E3 Drives		

Controllers	All Modules
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Serial Control	Supported on all Controllers
USB Control	Supported on all Controllers
REST Control	Supported on QTL1079 and QTL1461
Telnet Control	Supported on QTL1079 and QTL1461





Features	QTL2357	QTL2358	QTL2396	QTL2798	QTL2652	QTL2658	QTL2645	QTL2651
<b>Basic (power) hot/swap</b>	√	√	√	√	√	√	√	√
<b>Full hot-swap</b>	√	√	√	√	X	X	√	√
<b>Pin Bounce Simulation</b>	1uS minimum period				N/A		1uS minimum period	
<b>Signal Glitch</b>	Single/Cycle/PRBS				N/A		Single/Cycle/PRBS	
<b>Voltage Monitoring</b>	√	√	√	√	√	√	√	√
<b>Power Monitoring</b>	Requires Power Module				X	X	X	X
<b>Active Signal Driving</b>	PERST, WAKE, CLKREQ, PWRBRK				X	X	PERST, DUALPORT, IF_DET, PWR_DIS, PRSNT, HPT0, HPT1	
<b>Signal Monitoring</b>	PERST, WAKE, CLKREQ, PWRBRK, SMCLK, SMDAT				X	X	PERST, PERSTB, SMCLK, SMDAT, DUALPORT, IF_DET, ACTIVITY, WAKE, PWR_DIS, PRSNT, HPT0, HPT1	

Features...	QTL2645	QTL2651	QTL2757	QTL2661	QTL2662	QTL2686	QTL2692
<b>Basic (power) hot/swap</b>	√	√	√	√	√	√	√
<b>Full hot-swap</b>	√	√	X	√	√	√	√
<b>Pin Bounce Simulation</b>	1uS minimum period		X	1uS minimum period			
<b>Signal Glitch</b>	Single/Cycle/PRBS		X	Single/Cycle/PRBS			
<b>Voltage Monitoring</b>	√	√	√	√	√	√	√
<b>Power Monitoring</b>	X	X	X	X	X	X	X
<b>Active Signal Driving</b>	PERST, DUALPORT, IF_DET, PWR_DIS, PRSNT, HPT0, HPT1		X	PRSNT, PERST, PERSTB, DUALPORTEN, PWRDIS, IFDET, IFDET2, HPT0, HPT1		PRSNT0, PERST0, SMBRST, PWRDIS, MFG, DUALPORTEN	
<b>Signal Monitoring</b>	PERST, PERSTB, SMCLK, SMDAT, DUALPORT, IF_DET, ACTIVITY, WAKE, PWR_DIS, PRSNT, HPT0, HPT1		X	PERST, PERSTB, SMBCLK, SMBDAT, DUALPORT, IF_DET, IFDET2, HPT0, HPT1, PRSNT, WAKE, PWRDIS		PRSNT0, PERST0, SMBRST, SMBCLK, SMBDAT, PWRDIS, MFG, DUALPORTEN	

